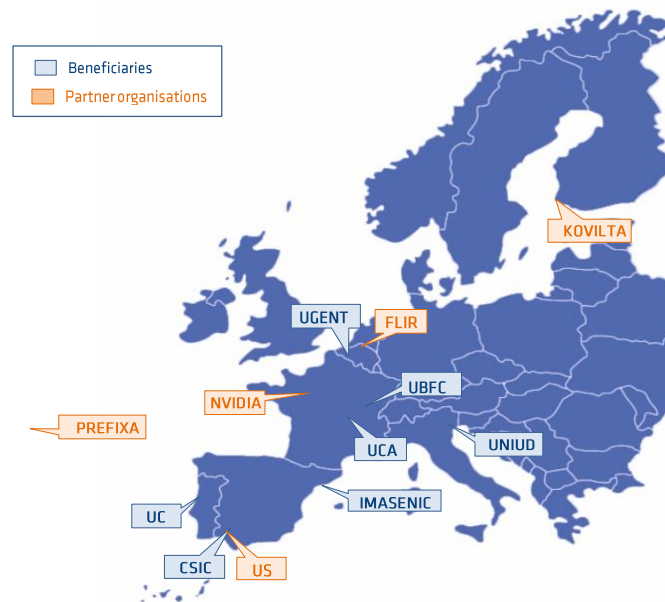
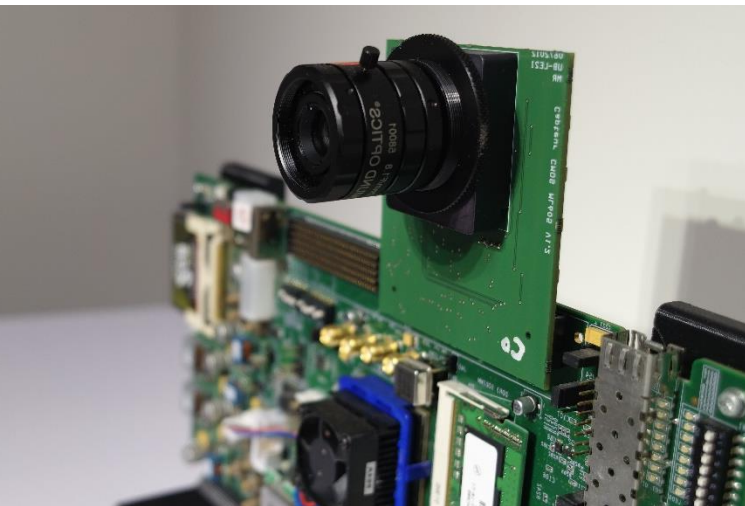


Overview



ACHIEVE-ITN is a H2020 MSCA Innovative Training Network for the research in Advanced Hardware and Software Components for Integrated and Embedded Vision Systems. It consists in coordinated research and training programmes for 9 Early Stage Researchers. They will carry out their individual research projects and some programmed secondments. Besides their work with academic and industrial partners, they will participate in network-wide training activities on transferable skills. This training programme is oriented to the transference of research results and the development of a highly-employable profile.



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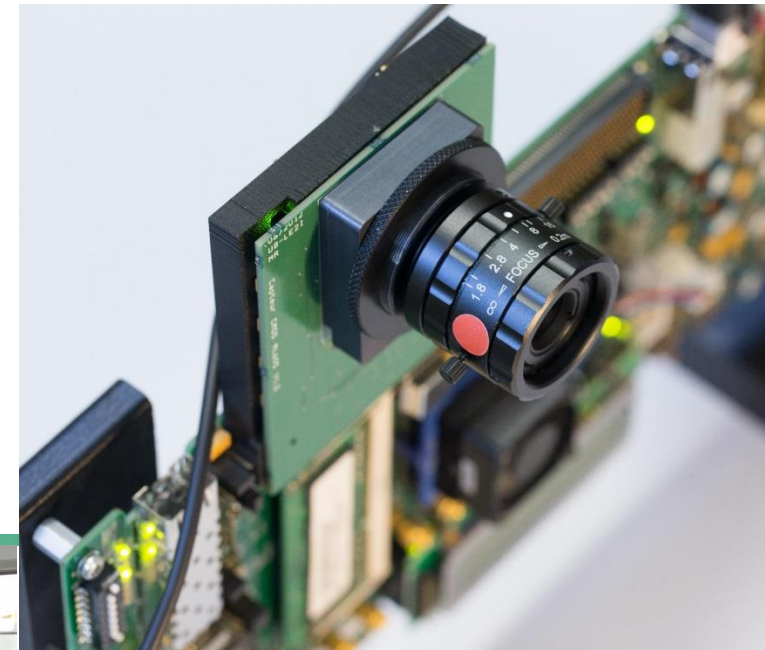
✉ rcarmona@imse-cnm.csic.es

🌐 www.achieve-itn.eu

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🌐 groups/13577032/

ACHIEVE-ITN

H2020 MSCA Innovative Training Network for the research on Advanced Hardware/Software Components for Integrated/Embedded Vision Systems



FEATURE TECHNOLOGY
INTEGRATED
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ACHIEVE-ITN
SOFTWARE
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DOCTORAL
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HARDWARE
EMBEDDED
INTELLIGENCE
CIRCUITS
VISION
LEARNING
INNOVATIVE
NETWORK
CHIPS
PHD
ARTIFICIAL

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Objectives



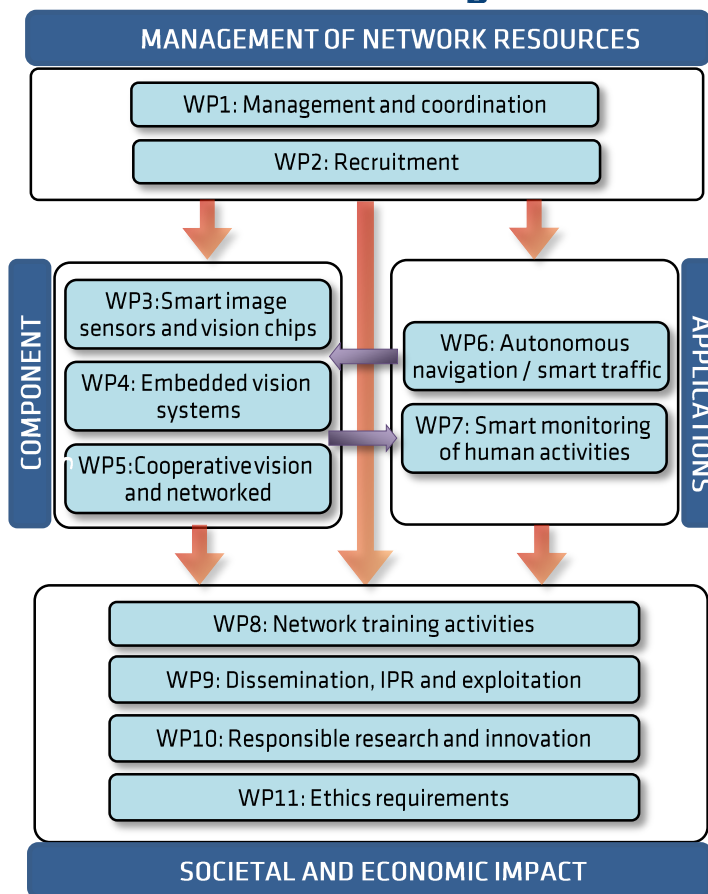
Research

- A distributed vision platform composed of networked, smart and efficient embedded vision systems.
- The basic infrastructure for application scenarios that demand cooperative vision based on in-node processing and extraction of relevant information.
- A de-centralized scheme for scalable systems to be easily deployable and resilient to partial failure.

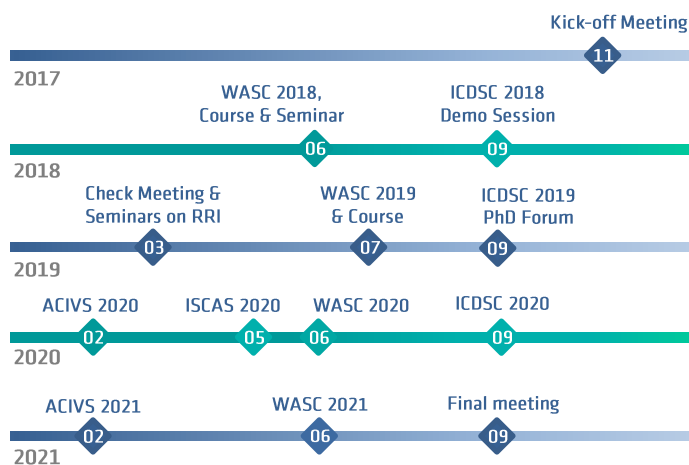
Training

- Acquisition of core research skills in PhD programmes at the host institutions
- Incorporation of additional/complementary research skills through the network-wide activities.
- Developing transferable skills related with scientific communication, responsible research and innovation, and technology transference.

Work Packages



Main training events & workshops



Individual Research Projects

- ESR1: Hardware acceleration of deep learning inference schemes
- ESR2: Light intensity/ToF sensors in CMOS technology and a 2D/3D smart cameras
- ESR3: Deep learning process integration on heterogeneous FPGA/GPU platform
- ESR4: Architectures for embedded deep learning techniques
- ESR5: Multi-camera embedded tracking and re-identification
- ESR6: Methods for autonomous navigation and localization in traffic environments
- ESR7: Cooperative tracking and visual analytics
- ESR8: Scene analysis for traffic monitoring
- ESR9: Development of smart, low-power CMOS Image Sensor

