lated from the empirical path occupancy rate \( r_i \) by the following relation:

\[
\lambda_n = \frac{r_i}{K - 1/n - 1} \quad n \neq 1
\]

where \( \lambda_n = r_i \). The path number distribution is then calculated successively, using the recursive formulas given for this model in Reference 6. Figs. 4 and 5 show a comparison between the empirical path number distributions and the modified Poisson distributions for the manufacturing floors and college campuses, respectively. The curve fittings show considerable improvement over those of the Poisson model.

\[\text{Fig. 5 Comparison of empirical and modified Poisson distribution for college laboratory areas}\]

This improvement is because the modified Poisson model utilizes the empirical probability of occupancy for each bin, whereas the Poisson model just uses the sum of the probabilities of occupancy for all bins. The optimum values of \( K \) calculated for the manufacturing floors and the campus environments are shown in Table I.

<table>
<thead>
<tr>
<th>Area</th>
<th>Number of locations</th>
<th>Number of bins ( N )</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>5</td>
<td>10</td>
</tr>
<tr>
<td>Manufacturing floors</td>
<td>288</td>
<td>1.184</td>
</tr>
<tr>
<td>College laboratories</td>
<td>196</td>
<td>0.892</td>
</tr>
</tbody>
</table>

Conclusions: Fading multipath profiles obtained from several different manufacturing floors and some college laboratories at 910 MHz, have been used to model the arrival of the paths. The empirical distribution was compared with the Poisson and the modified Poisson distributions and the modified Poisson model is shown to provide a better fit. This observation indicates that the paths tend to arrive in clusters rather than in a random manner.

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References


**10MHz CMOS OTA-C VOLTAGE-CONTROLLED QUADRATURE OSCILLATOR**

Indexing terms: Circuit theory and design, Oscillators, Integrated circuits, Nonlinear networks

A quadrature-type voltage-controlled oscillator with operational transconductance amplifiers and capacitors (OTA-C) is presented. A monolithic integrated CMOS test circuit is introduced to verify theoretical results. The attainable frequency range of oscillation of the chip test circuit is 3–10.34 MHz. The total harmonic distortion (THD) is 0.20–1.87% for corresponding peak-to-peak amplitude voltages between 100 mV and 1 V. This amplitude can be controlled either by using a diode connection of two MOS transistors or a proposed nonlinear resistor.

**Introduction**: The quadrature mode of operation is a well-known mode of waveform generation in the field of sinewave oscillators. Two sinewaves in quadrature, that is with 90° phase difference, are generated. This is accomplished with a two-integrator loop. Besides this, some form of regeneration is usually included to ensure that the oscillation is created. The poles are initially located in the right half-plane (RHP) of the complex frequency plane and then pulled back by a nonlinear amplitude limiter.

To make a quadrature oscillator into a voltage-controlled oscillator (VCO), the integrators can be implemented using OTAs. The output current of a CMOS-OTA is controlled by the differential input voltage and its transconductance gain \( g_m \). Furthermore, \( g_m \) can be varied over several octaves by adjusting an external DC amplifier bias current \( I_{eb} \), i.e.

\[ g_m = h_{ce} / (I_{eb}) \]

where \( h_{ce} \) is a process-dependent parameter.

In this letter an integrated CMOS OTA-C quadrature VCO using a 3 µm double metal technology (processed by MOSIS, Marina del Rey, CA) is presented. The frequency of oscillation can be externally adjusted by nearly two octaves with a THD less than 1.9%. The real part of the poles can be adjusted, in practice, independently of the frequency of oscillation.

**Theory**: The proposed quadrature VCO architecture is shown in Fig. 1. It has one inverting \( g_{m1}(C_1) \) and one noninverting integrator \( g_{m2}(C_2) \). \( R_3 \) is a nonlinear resistor whose \( i/v \) characteristics are depicted in Fig. 2a. \( R_3 \) provides the nonlinear amplitude limiter function. Implementations are discussed later. The OTAs associated with \( g_{m1} \) and \( g_{m2} \) allow

**Fig. 1 Proposed OTA-C quadrature oscillator structure**

**Theory**: The proposed quadrature VCO architecture is shown in Fig. 1. It has one inverting \( g_{m1}(C_1) \) and one noninverting integrator \( g_{m2}(C_2) \). \( R_3 \) is a nonlinear resistor whose \( i/v \) characteristics are depicted in Fig. 2a. \( R_3 \) provides the nonlinear amplitude limiter function. Implementations are discussed later. The OTAs associated with \( g_{m1} \) and \( g_{m2} \) allow
placing the poles in the RHP. The characteristic equation of the oscillator is given by

\[ s^2 + \frac{1}{C_2} \left[ G(N) - (g_m - g_m') \right] + \frac{g_m' \theta_2}{C_1 C_2} = 0 \]  

(2)

where \( G(N) \) is the equivalent conductance of \( R_s \) using the describing function. \(^{\text{b}}\) Thus, for \( I_C = A \cos \omega t \) and \( I_1 = f(V_C) \), it yields

\[ N(A) = \frac{1}{A T} \int_0^T f(A \cos \omega t) \cos \omega t \, dt \]  

(3)

where \( T = 2\pi/\omega \). For a transfer characteristic as shown in Fig. 2a, we obtain

\[ N(A) = 0 \quad \text{for} \quad A < E \]  

(4a)

\[ N(A) = 1 \quad \text{for} \quad A > E \]  

(4b)

therefore, when \( A = 0 \) and making \( g_m' > g_m \), the roots of eqn. 2 are in the RHP and the oscillator will be self-starting. As the amplitude of oscillation increases \( G(N) \) also increases, and the roots are pulled back toward the imaginary axis.

![Fig. 2 Nonlinear resistor](image)

**a** Input characteristics  
**b** Simple \( R_s \) implementation  
**c** Proposed nonlinear resistor

Furthermore, when \( G(N) = g_m' - g_m \), it is guaranteed that the roots will reach the imaginary axis for some value of \( A \) and hence a stable limit cycle is reached. This cycle is characterised by

\[ \omega = \left( \frac{g_m' \theta_2}{C_1 C_2} \right)^{1/2} \]  

(5)

\[ A = \frac{1}{N} \left( \frac{g_m' - g_m}{G} \right) \]  

(6)

CMOS implementations of \( R_s \): Two alternatives are discussed; the first one, shown in Fig. 2b, is simply a diode connection of two MOS transistors. For the second implementation the principle of operation is illustrated in Fig. 2c. If \( V_C < E \) then \( V_n = -V_S \) and transistor \( M_s \) is in cutoff \( (i_c = 0) \). If \( V_C > E \), \( V_n \) becomes positive and \( M_s \) is on and capable of driving a large current. For implementing the complete characteristic of Fig. 2a, a similar circuit (Fig. 2c) is added, with \( M_s \) being a PMOS. Since the performance of the comparator is not critical, a simple differential input to a single-ended output amplifier \(^{\text{c}}\) is used.

**Experimental results:** A 3 \( \mu \)m CMOS process (through and thanks to MOSIS) test prototype was fabricated. The capacitors \( C_1 \) and \( C_2 \) were not physically implemented, instead the parasitic capacitances associated with nodes \( A \) and \( B \) of Fig. 1 were used. Fig. 3 shows a photomicrograph of the test chip. The linearised OTA used is similar to the one presented by Nedungadi and Geiger \(^{\text{d}}\) with transconductance increased by a factor of four. The range of the \( g_m \)s of the OTA is 8–238 \( \mu \)mho. For an output amplitude of 1 V peak-to-peak, the frequency range is 3 to 10.34 MHz, and the THD is kept below 2%. When reducing the oscillation amplitude, the distortion is lowered, i.e. for 100 \( \mu \)V peak-to-peak, the measured THD at 10 MHz was 0.2%. Fig. 4 shows the measured spectrum for an oscillation frequency of 10 MHz and 1 V peak-to-peak amplitude, using a nonlinear resistor implementation based on Fig. 2c.

![Fig. 3 Photomicrograph of test chip](image)

![Fig. 4 Spectrum of \( V_{out} = 1V \times (2x \times 10^3) \) using \( R_s \) of Fig. 2c](image)

Conclusions: A versatile voltage-controlled OTA-C quadrature oscillator with a novel nonlinear amplitude limiter has been introduced. An experimental trade-off between THD and the amplitude of the oscillating sinewave has been shown. A good high-frequency range was obtained using a conventional 3 \( \mu \)m CMOS (double-metal) process. The voltage-control property makes it possible to have the oscillator tuned on chip for different applications and/or temperature and process variations. The proposed OTA-C oscillator architecture has potential to operate at higher frequencies by using a better CMOS process and/or an improved OTA design.

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COMPRESS AND DISPERSIONLESS TRANSMISSION OF CHIRPED 1.3-μm DFB PS LASER PULSES IN DISPERSION-SHIFTED SINGLE-MODE FIBRES

Indexing terms: Optical communications, Semiconductor lasers, Optical dispersion, Optical transmission

Optical pulse compression of high-speed gain-switched ps 1.3-μm DFB semiconductor laser pulses in dispersion-shifted single-mode fibres is measured with a streak camera. The laser red-shift chirp is compensated for by the negative chromatic fibre dispersion, resulting in pulse compression and 'dispersionless' transmission of 45 ps pulses over 12 km of fibre.

Introduction: Optical wavelength chirping of the individual longitudinal modes of a gain-switched or high-speed modulated semiconductor laser has been previously observed in time-resolved studies. The chirp-induced dispersion penalty is one of the important factors which eventually limits the high-speed transmission of DFB laser signals. Several schemes have been proposed to reduce chirp in DFB lasers for high-speed transmission in dispersive fibres. The use of external modulation, modulation-pulse-shaping, CW-injection locking and special reduced-chirp laser cavity designs have been reported. An entirely different approach, instead of trying to eliminate the chirp, is to control the chirp and use it to advantage in fibre transmission. We report the experimental realisation of an earlier proposal which utilises the chirped DFB laser pulse compression to achieve 'dispersionless' transmission in dispersion-shifted single-mode fibres (DS-SMF). In this scheme, the fibre's dispersion is negative at the laser wavelength and so longer wavelengths propagate faster and the red-shift chirp is compensated for, resulting in pulse compression and subsequent rebroadening in the fibre. The fibre distance over which the optical input pulse is initially compressed and later rebroadened to its input pulse width is called the 'dispersionless' distance \( L_o \). We report measurements of \( L_o \) in DS-SMF using high-speed ps pulses from a 1.3-μm DFB laser.

Experiment: The experimental set-up is shown in Fig. 1. The DFB laser is gain-switched with the signal from the comb generator, which is driven by a sinewave (1, 2 or 3 GHz) of 5-8 V amplitude, providing output pulse widths in the 23-45 ps range (simulating 10-20 Gbit/s modulation speeds). The wavelength and threshold current of the laser are \( \lambda_p = 1.29 \mu m \) and \( I_p = 29.0 \) mA, respectively. The optical signal is detected by a Hamamatsu synchroscan streak camera (risetime < 10 ps). The DS-SMF dispersion is \( -22 \) ps/nm/km at \( \lambda_p = 1.29 \mu m \) with the zero dispersion at \( \lambda_p = 1.55 \mu m \). At 1, 2 and 3 GHz repetition rates the laser was biased at \( I_{dc} = 30.4, 36.8 \) and \( 41.3 \) mA and modulated with \( I_p = 155, 92, \) and \( 46 \) mA, resulting in peak powers of 5.4, 4.2 and \( 2.7 \) mW, respectively, in the fibre pigtail. Fig. 2 shows the pulse waveforms at various fibre lengths for the 3 GHz repetition rate. It is seen that the initial laser pulse (FWHM = 45 ps) is compressed to 15 ps after transmission over 5 km of fibre after which it starts to rebroaden. After transmission over 10 and 14 km of fibre a shoulder appears in the pulse due to the overtaking of the 'red' wavelengths in the beginning of the pulse. Fig. 3 shows the pulse width variation against the fibre length and the total dispersion for the three pulse repetition rates. The dispersionless transmission distance for the 3 GHz pulses is \( L_o = 12 \) km. The results of these measurements are consistent with theoretical results. For a laser with \( L_o \) closer to \( L_o \), say \( \lambda_p = 1.53 \mu m \), the dispersionless distance becomes 156 km. This is simply because a longer fibre length is required to compensate for the much smaller fibre dispersion at 1.53 μm, and the transmission then becomes loss-limited. Fig. 3 also shows that \( L_o \) increases with the pulse width. This is due to a smaller chirp width at the higher repetition rates, which is attributed to the lower pump currents in our particular case. As a consequence, a broader optical pulse with a smaller chirp width of 0.30 nm (-6 dB down) is obtained at 3 GHz, as compared with the chirp widths of 1.20 and 0.85 nm at 1 and 2 GHz, respectively. We also examined the effect of decreasing the bias current at 3 GHz modulation. This resulted in a significantly larger chirp width. In addition we noticed the effect of lowering the pump current for the case of