

Publications – Jose M. de la Rosa

BOOKS:

- [1] **J.M. de la Rosa**, B. Pérez-Verdú and A. Rodríguez-Vázquez (Preface by C. Toumazou). *Systematic Design of CMOS Switched-Current Bandpass Sigma-Delta Modulators for Digital Communication Chips*, ISBN 0-7923-7678-1, Kluwer Academic Publishers 2002.
- [2] J.F. López, F.V. Fernández, J.M. López-Villegas and **J.M. de la Rosa** (Editors). *VLSI Circuits and Systems*, ISBN 0-8194-5832-5, SPIE 2005.
- [3] R. del Río, F. Medeiro, B. Pérez-Verdú, **J.M. de la Rosa** and A. Rodríguez-Vázquez. *CMOS Cascade Sigma-Delta Modulators for Sensors and Telecom: Error Analysis and Practical Design*, ISBN: 1-4020-4775-4. Springer, 2006.

BOOK CHAPTERS:

- [1] F. Medeiro, **J.M. de la Rosa**, B. Pérez-Verdú and A. Rodríguez-Vázquez: “Tools for Automated Design of $\Sigma\Delta$ Modulators”. *Chapter 12 in Analog Circuit Design: MOST RF Circuits, Sigma-Delta Converters and Translinear Circuits* (edited by W. Sansen, J. Huijsing and R. van de Plassche), ISBN 0-7923-9776-2, Kluwer Academic Publishers 1997.
- [2] A. Rodríguez-Vázquez, M. Delgado-Restituto, R. Domínguez-Castro and **J.M. de la Rosa**: “Comparator Circuits”. at *Wiley Encyclopedia of Electrical and Electronics Engineering* (edited by John G. Webster), ISBN 0-471-13946-3, Vol. 3, pp. 577-600, John Wiley & Sons, New-York 1999.
- [3] A. Rodríguez-Vázquez, M. Delgado-Restituto, R. Domínguez-Castro and **J.M. de la Rosa**: “Trade-Offs in the Design of CMOS Comparators”. *Chapter 15 in Trade-Offs in Analog Circuit Design: The Designer’s Companion* (edited by C. Toumazou, G. S. Moschytz and B. Gilbert), ISBN 0-4020-7037-3, Kluwer Academic Publishers 2002.
- [4] R. del Río, F. Medeiro, **J.M. de la Rosa**, B. Pérez-Verdú and A. Rodríguez-Vázquez: “Correction-Free Multi-Bit Sigma-Delta Modulators for ADSL”. In *“Analog Circuit Design: Structured Mixed-Mode Design, Multi-Bit $\Sigma\Delta$ Converters, Short Range RF Circuits”* (Edited by M. Steyaert, A. Roermund and J. Huijsing), ISBN: 1-4020-7216-3, Kluwer Academic Publishers, 2002.
- [5] A. Rodríguez-Vázquez, R. del Río, **J.M. de la Rosa**, F. Medeiro and B. Pérez-Verdú: “Sigma-Delta CMOS ADCs: An Overview of the State-of-the-Art”. *Chapter 2 in CMOS Telecom Data Converters* (Edited by A. Rodríguez-Vázquez, F. Medeiro and E. Janssens), ISBN: 1-4020-7546-4, Kluwer Academic Publishers, 2003.
- [6] R. Domínguez-Castro, A. Rodríguez-Vázquez, **J. M. de la Rosa**, M. Delgado-Restituto, and F. Medeiro: “CMOS Comparators”. *Chapter 4 in CMOS Telecom Data Converters* (Edited by A. Rodríguez-Vázquez, F. Medeiro and E. Janssens), ISBN: 1-4020-7546-4, Kluwer Academic Publishers, 2003.
- [7] F. Medeiro, R. del Río, **J.M. de la Rosa**, B. Pérez-Verdú and A. Rodríguez-Vázquez: “High-Order Cascade Multi-bit $\Sigma\Delta$ Modulators”. *Chapter 9 in CMOS Telecom Data Converters* (Edited by A. Rodríguez-Vázquez, F. Medeiro and E. Janssens), ISBN: 1-4020-7546-4, Kluwer Academic Publishers, 2003.
- [8] **J. M. de la Rosa**, B. Pérez-Verdú, F. Medeiro, R. del Río and A. Rodríguez-Vázquez: “Bandpass Sigma-Delta Analog-to-Digital Converters”. *Chapter 11 in CMOS Telecom Data Converters* (Edited by A. Rodríguez-Vázquez, F. Medeiro and E. Janssens), ISBN: 1-4020-7546-4, Kluwer Academic Publishers, 2003.